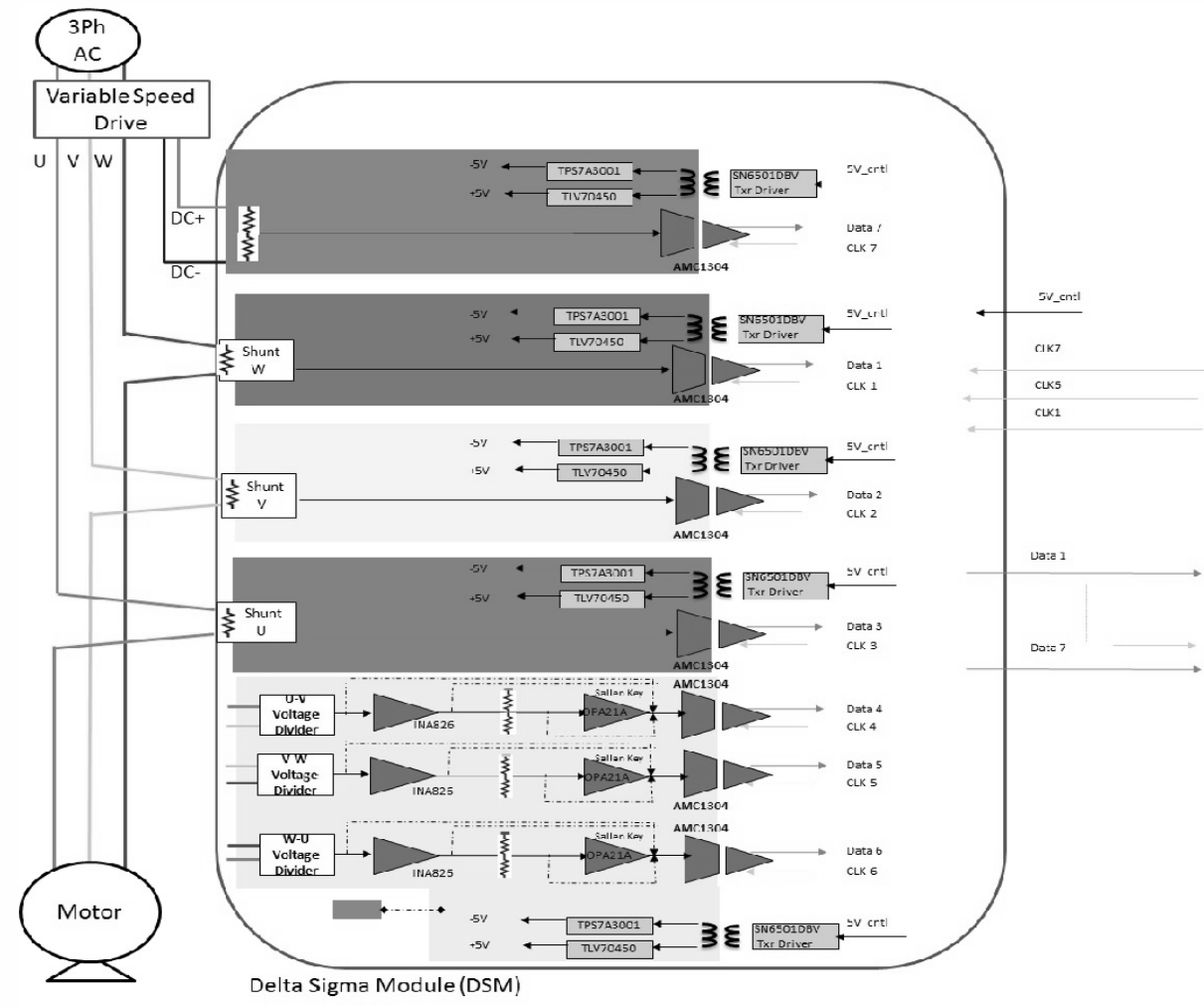
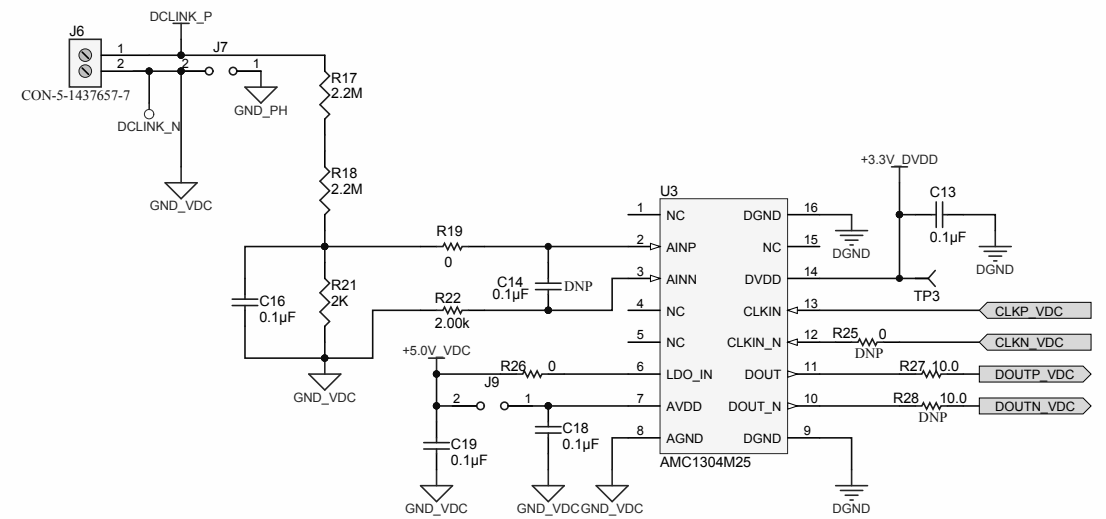
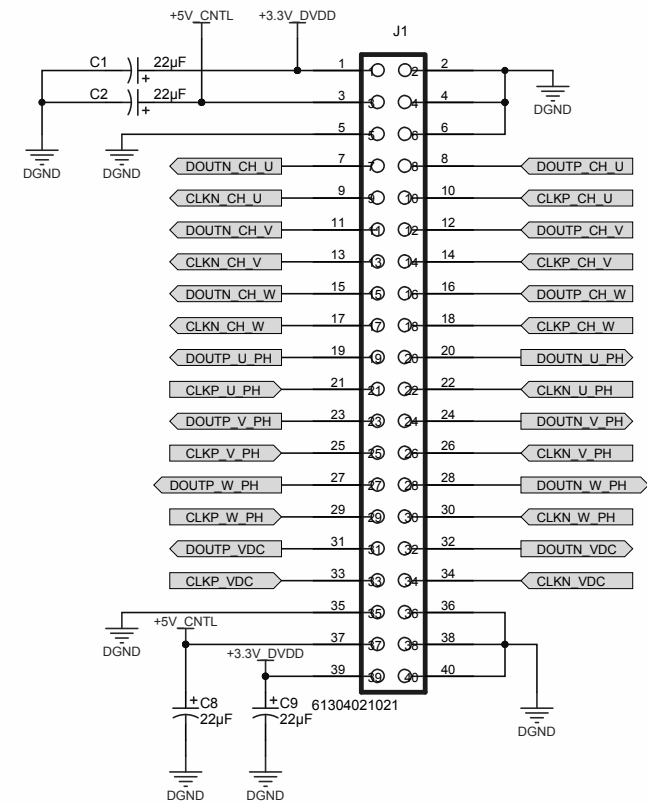
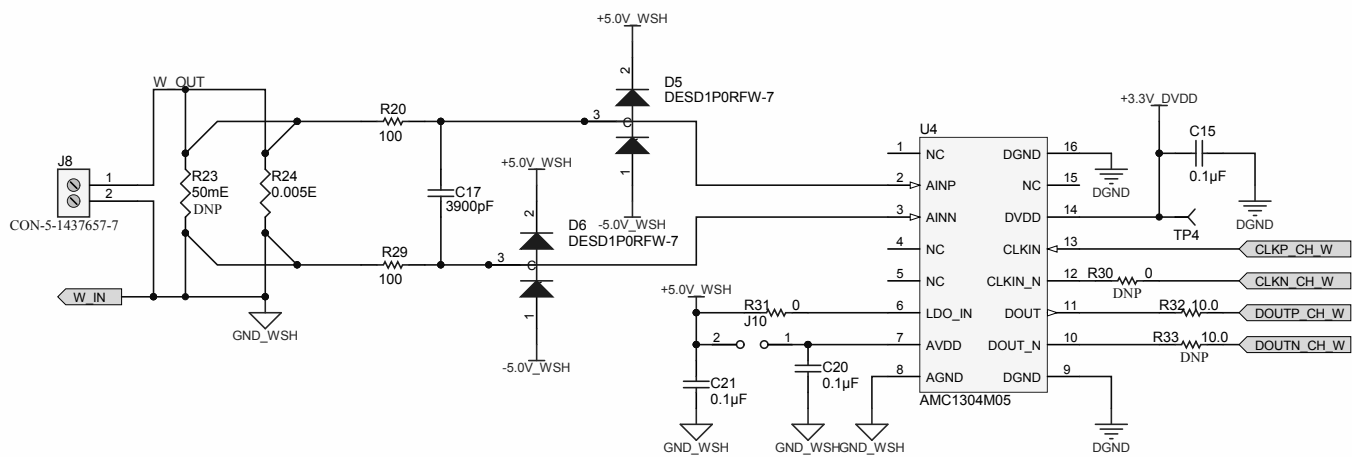
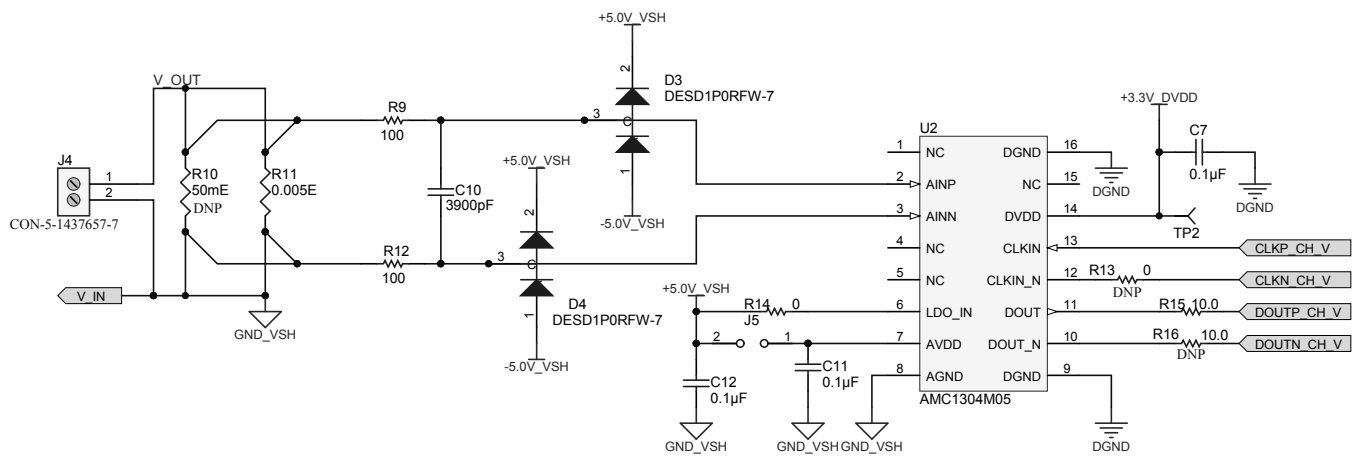
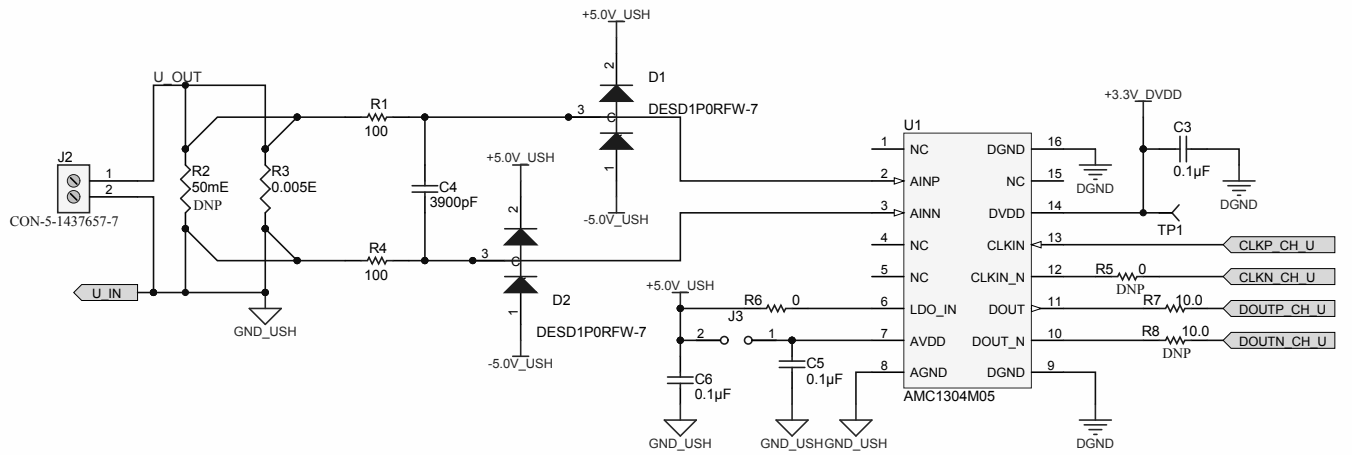


Page 2	Block Diagram
Page 3	Current Sense
Page 4	Voltage signal condtioning
Page 5	Power Supply1
Page 6	Power Supply2
Page 7	Hardware - MIscellaneous

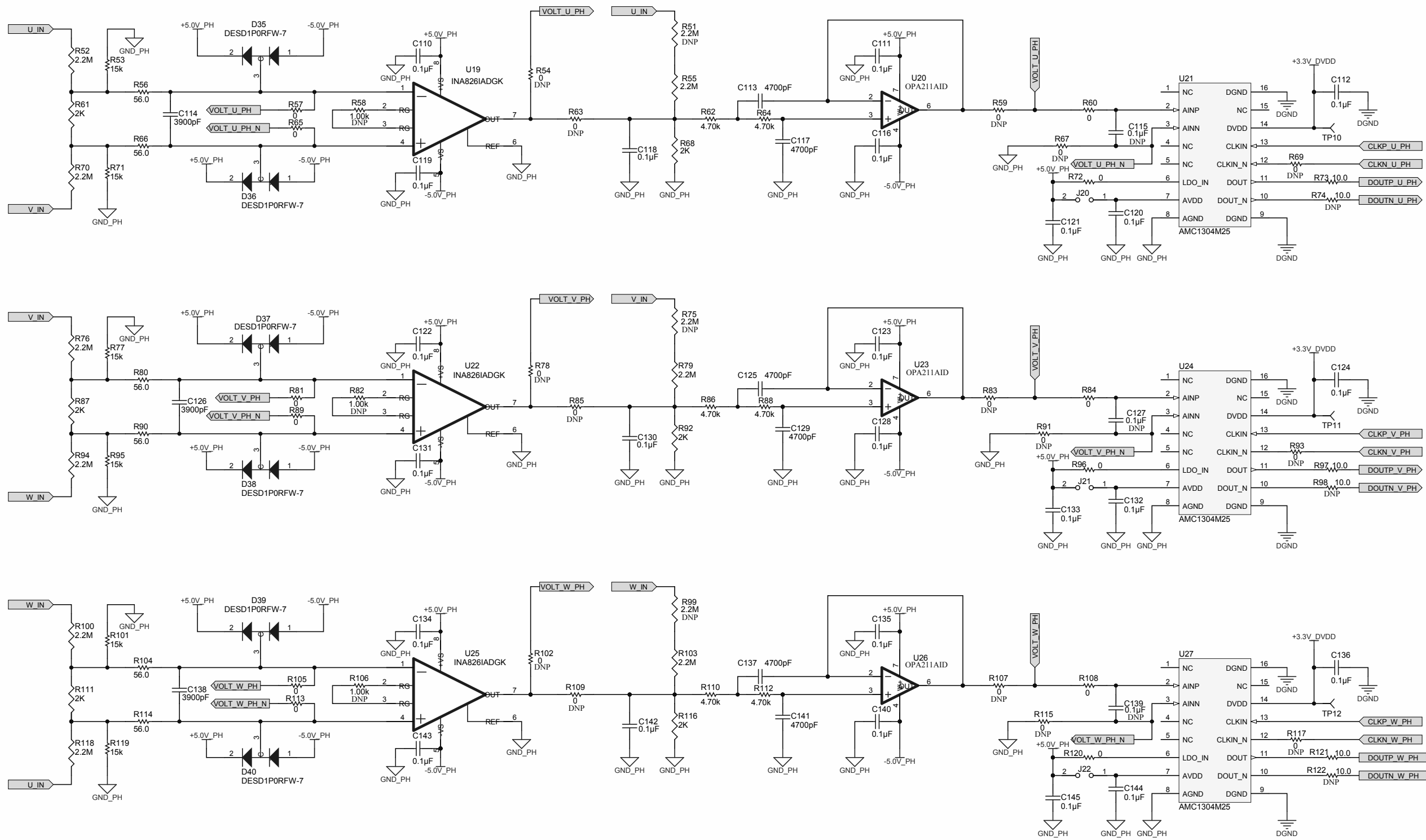
Revision History	
Revision	Notes



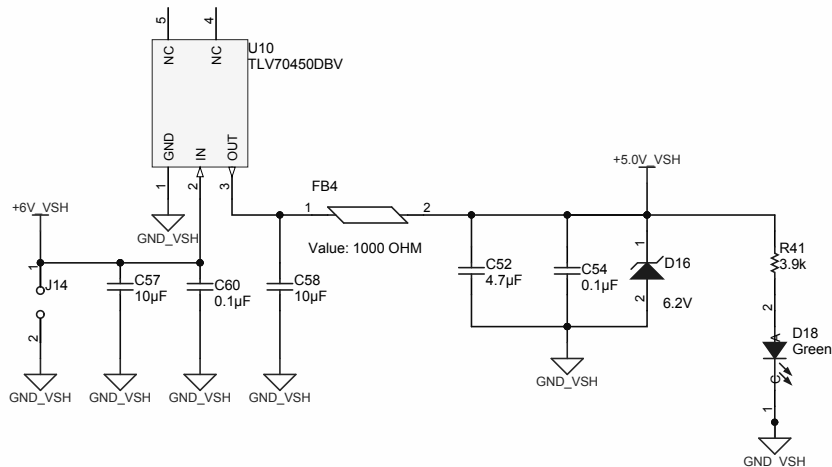
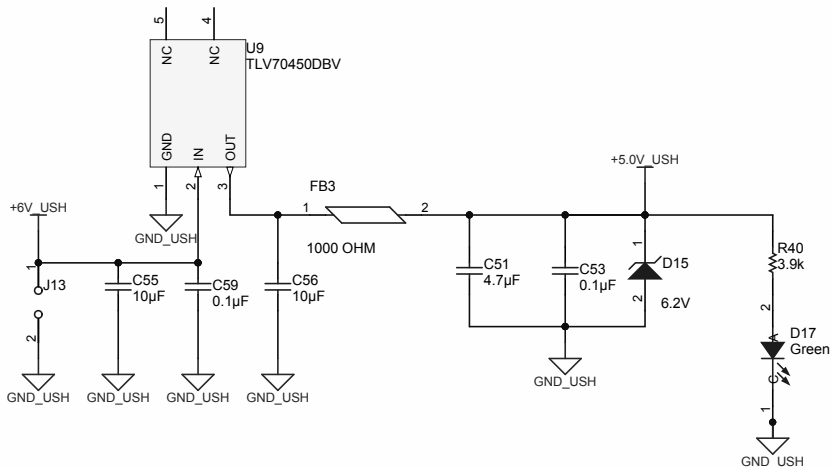
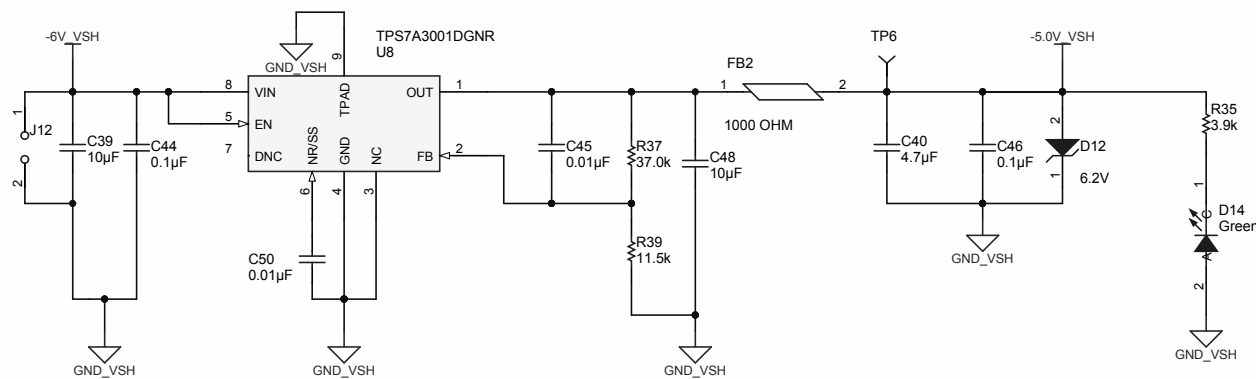
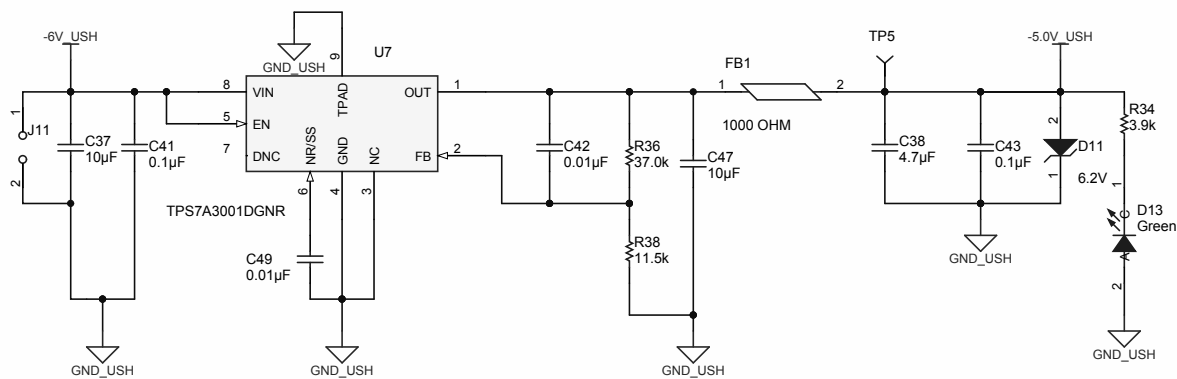
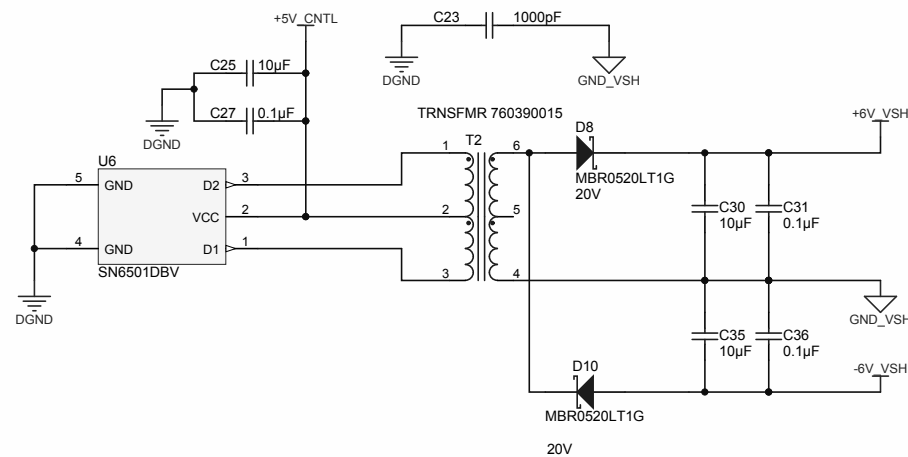
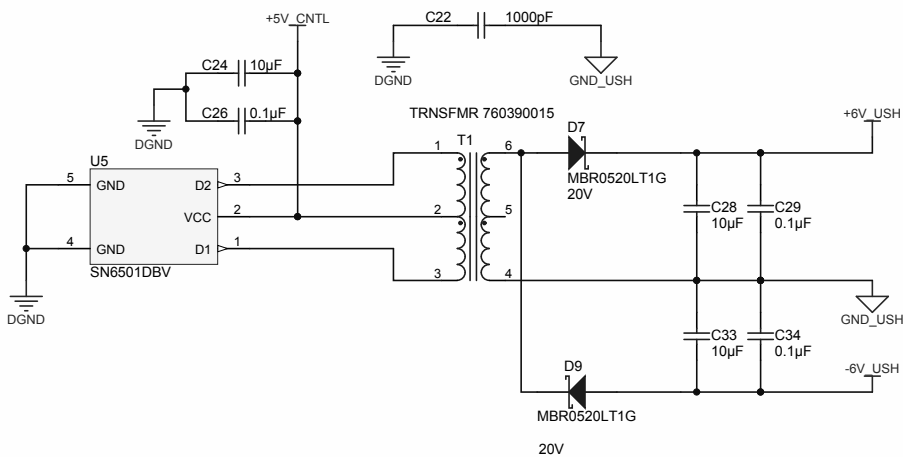
Dotted line represent alternate connection options



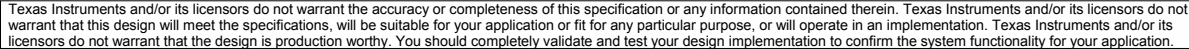
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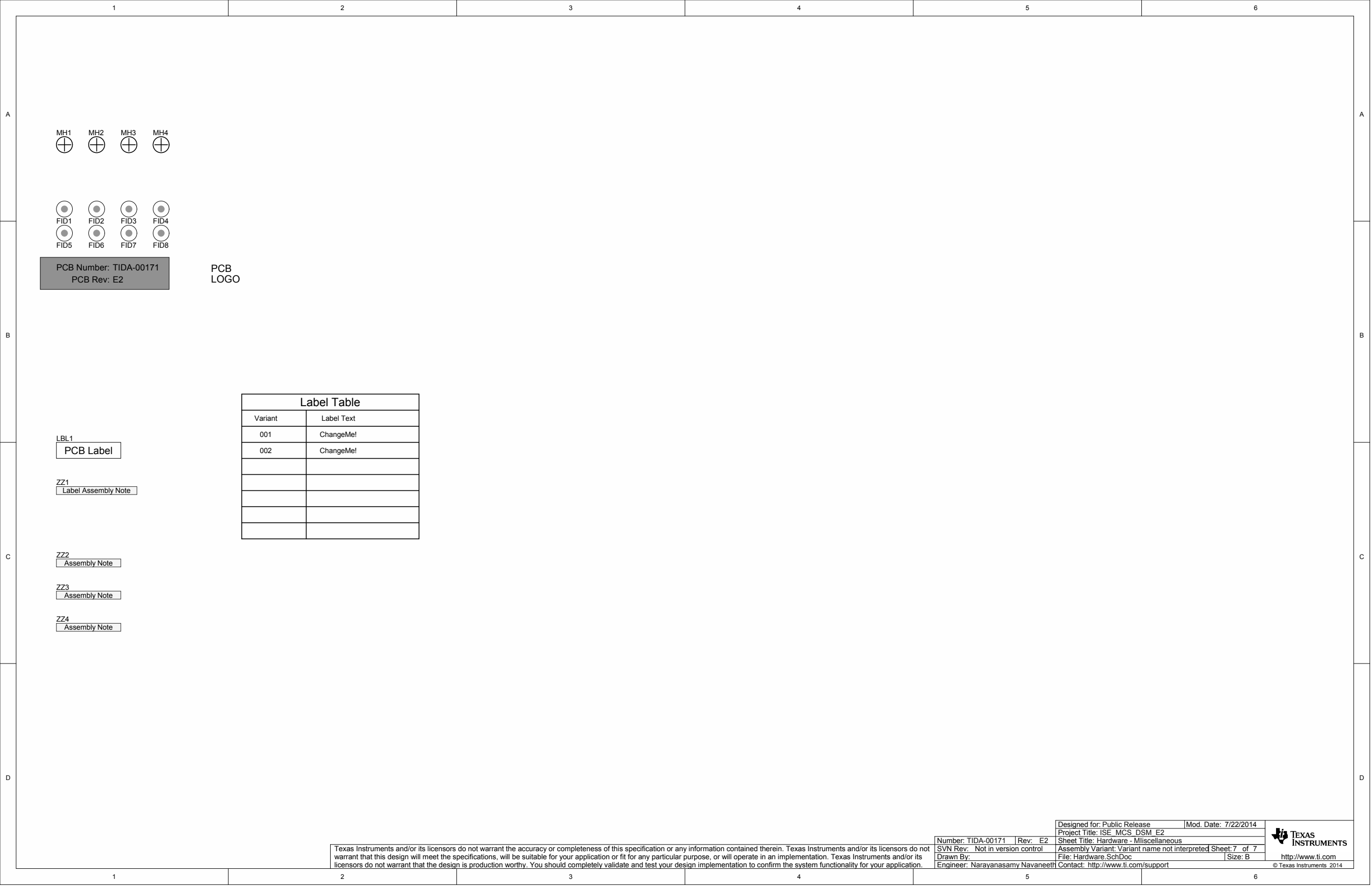


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Designed for: Public Release		Mod. Date: 7/22/2014	
Project Title: ISE MCS DSM E2			
Sheet Title: Hardware - Miscellaneous			
Number: TIDA-00171	Rev: E2	Assembly Variant: Variant name not interpreted	Sheet: 7 of 7
SVN Rev: Not in version control	File: Hardware.SchDoc		Size: B
Drawn By:	Engineer: Narayanasamy Navaneeth		Contact: http://www.ti.com/support

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